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| APPLICATION NUMBER | FILING or 371(c) DATE | GRP ART UNIT | FIL FEE REC'D | ATTY.DOCKET.NO | TOT CLAIMS | IND CLAIMS |
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CONFIRMATION NO. 9470

23353
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FILING RECEIPT

Date Mailed: 03/21/2007

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Applicant(s)

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Assignment For Published Patent Application

SONY CORPORATION, TOKYO, JAPAN

Power of Attorney: The patent practitioners associated with Customer Number 23353

Domestic Priority data as claimed by applicant

This application is a 371 of PCT/JP04/17529 11/18/2004

Foreign Applications

JAPAN P2003-401274 12/01/2003

If Required, Foreign Filing License Granted: 01/16/2007

The country code and number of your priority application, to be used for filing abroad under the Paris Convention, is

US10/581,076

Projected Publication Date: 04/26/2007

Non-Publication Request: No

Early Publication Request: No

Title

Clocked inverter circuit, latch circuit, shift register circuit, drive circuit for display apparatus, and display apparatus

Preliminary Class

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